Thesis/Project proposal

Name	Affinity
Contacts	Luigi Pomante <luigi.pomante@univaq.it> Vittoriano Muttillo <vittoriano.muttillo@univaq.it> Paolo Giammatteo (paolo.giammateo@univaq.it)
Type	Implementation, Research
Keywords	HW/SW Co-Design, DSE, System-Level Metric
Description	The work focuses on the extension of a system-level metric for HW/SW technologies, called *Affinity*. The goals are: - to improve the metric to better consider GPP/DSP/SPP (also analyzing SoA) - to extend the metric to consider also GPU - to develop tools for the automatic evaluation of the metric (with focus on Machine Learning approaches)
Expected Duration	2-6 months
References (Online)	L. Pomanbte, W. Fornaciari, F. Salice, D. Sciuto. "Metrics for Design Space Exploration of Heterogeneous Multiprocessor Embedded System", *IEEE/ACM International Workshop on Hardware Software Co-Design* (IEEE/ACM CODES 2002), Colorado (USA), Maggio 2002. L. Pomante, D. Sciuto, F. Salice, W. Fornaciari, C. Brandolese. "Affinity-Driven System Design Exploration for Heterogeneous Multiprocessor SoC", IEEE Transactions on Computers, vol. 55, no. 5, Maggio 2006.
References (Attached)	