

# Embedded Systems Design: A Unified Hardware/Software Introduction

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## Introduction

ESD\_Cap1++

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# Outline

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- Embedded systems overview
  - What are they?
- Design challenge – optimizing design metrics
- Technologies
  - IC technologies
  - Processor technologies
  - Design technologies
- Summary

# Embedded Systems Design: A Unified Hardware/Software Introduction

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Embedded systems overview

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# Embedded systems overview

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- Computing systems are everywhere
- Most of us think of “desktop” computers
  - PC’s 
  - Laptops 
  - Mainframes
  - Servers
- But there’s another type of computing system
  - Far more common...

# Embedded systems overview

- Embedded computing systems
  - Computing systems embedded within other devices
  - Hard to define. Nearly any computing system other than a desktop computer
  - Billions of units produced yearly, versus millions of desktop units
  - Perhaps 50 per household and per automobile

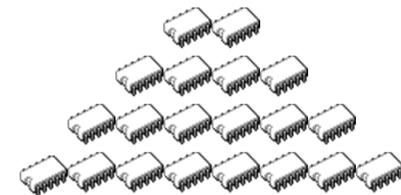
Computers are in here...



and here...



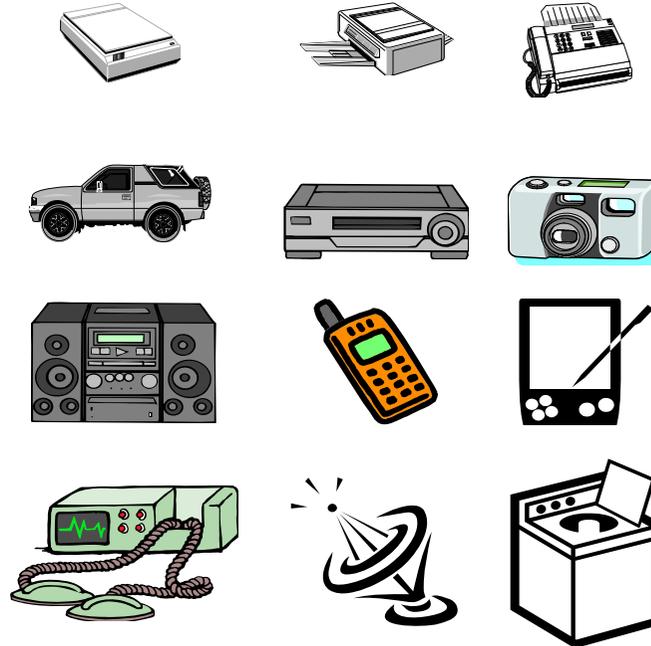
and even here...



Lots more of these,  
though they cost a lot  
less each.

# A “short list” of embedded systems

Anti-lock brakes	Modems
Auto-focus cameras	MPEG decoders
Automatic teller machines	Network cards
Automatic toll systems	Network switches/routers
Automatic transmission	On-board navigation
Avionic systems	Pagers
Battery chargers	Photocopiers
Camcorders	Point-of-sale systems
Cell phones	Portable video games
Cell-phone base stations	Printers
Cordless phones	Satellite phones
Cruise control	Scanners
Curbside check-in systems	Smart ovens/dishwashers
Digital cameras	Speech recognizers
Disk drives	Stereo systems
Electronic card readers	Teleconferencing systems
Electronic instruments	Televisions
Electronic toys/games	Temperature controllers
Factory control	Theft tracking systems
Fax machines	TV set-top boxes
Fingerprint identifiers	VCR's, DVD players
Home security systems	Video game consoles
Life-support systems	Video phones
Medical testing systems	Washers and dryers



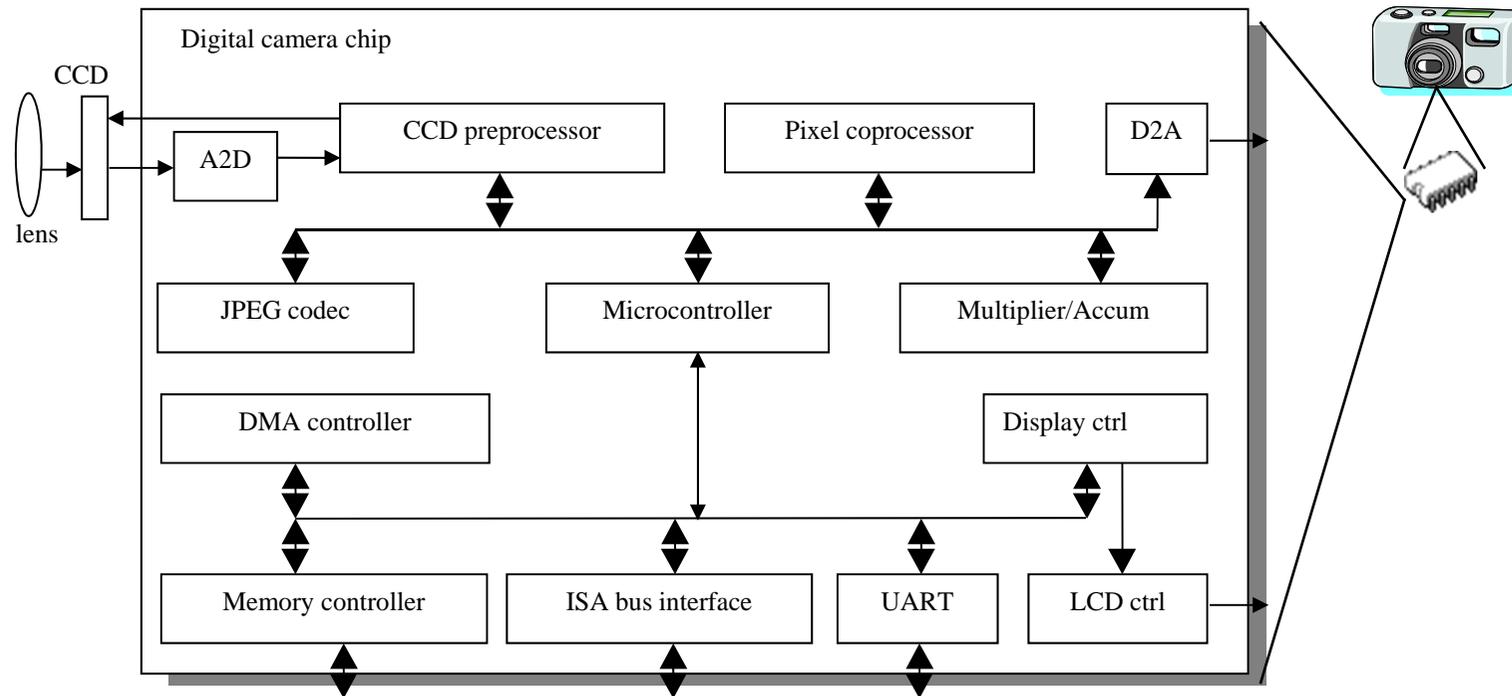
And the list goes on and on

# Some common characteristics of embedded systems

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- Single-functioned
  - Executes a single program, repeatedly
- Tightly-constrained
  - Low cost, low power, small, fast, etc.
- Reactive and real-time
  - Continually reacts to changes in the system's environment
  - Must compute certain results in “real-time” without delay

# An embedded system example: a digital camera



- Single-functioned -- always a digital camera
- Tightly-constrained -- Low cost, low power, small, fast
- Reactive and real-time -- only to a small extent

# Considerations

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- An important feature of such systems, from a market point of view, is that they are often used to introduce innovation
  - Traditional products with innovative services

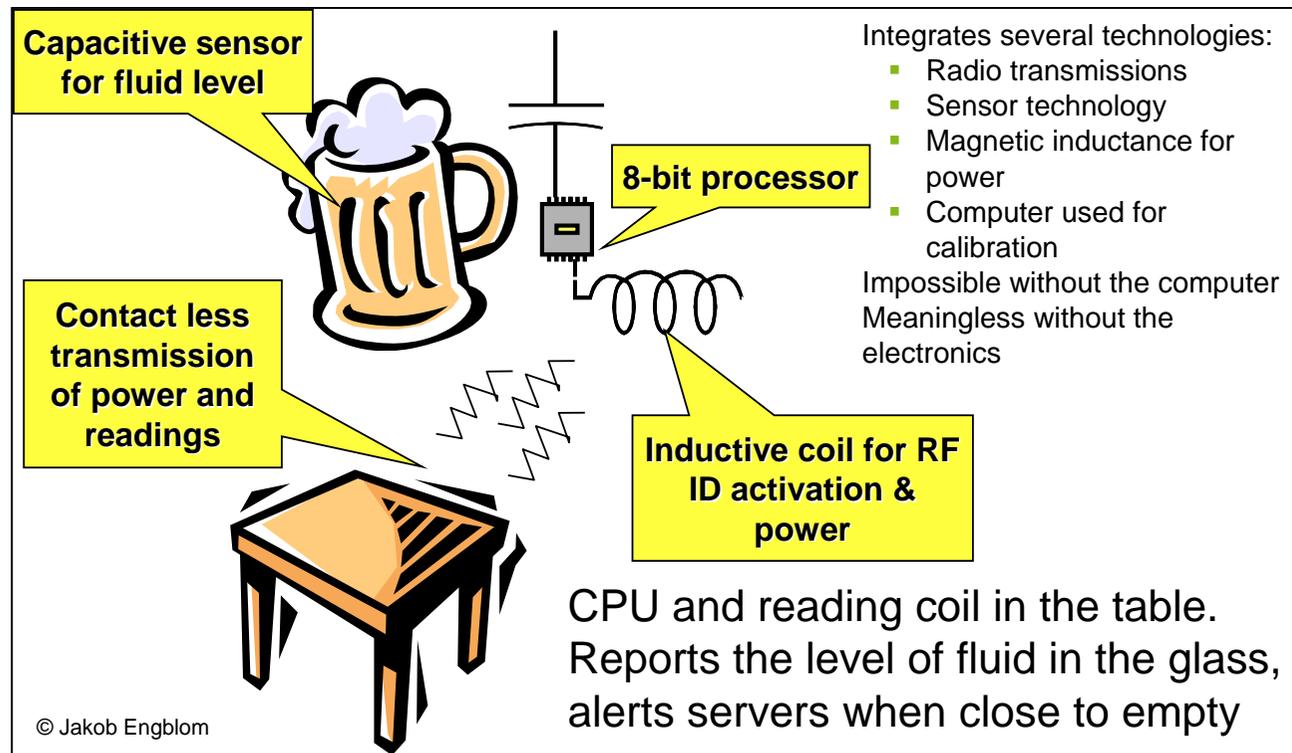
# Considerations

- Traditional products with innovative services
  - Example: *E-Tooth-Brush*



# Considerations

- Traditional products with innovative services
  - Example: *Smart Beer Glass*



# Considerations

- Traditional products with innovative services
  - Example: *Remember Ring*



# Considerations

- Traditional products with innovative services
  - Example: *OSRAM DULUX EL SENSOR*

**ON / OFF automatico:  
OSRAM DULUX® EL SENSOR.**

OSRAM DULUX® EL SENSOR è ideale per l'illuminazione automatica: grazie ai due sensori incorporati si accende all'imbrunire e si spegne automaticamente con la luce del giorno.

- Controllo a microprocessore che distingue la luce naturale da quella di altre sorgenti luminose
- Funzione autotest che si attiva automaticamente dopo l'accensione
- Sensore intelligente che riconosce la composizione spettrale della luce naturale e non è influenzato dalla luce della lampada stessa
- Durata media extra lunga, 15.000 ore
- Circa 500.000 cicli di accensione/spegnimento



The image shows a white OSRAM DULUX EL SENSOR light bulb and its retail packaging. The packaging is green and white, featuring the OSRAM logo and the text '15 000 h' and '75+'. A small icon on the left side of the packaging shows a light bulb with a sun and moon, indicating its automatic on/off functionality. The bulb itself is a compact fluorescent bulb (CFL) with a white base and a clear glass envelope.

# Considerations

- Traditional products with innovative services

- And so on...

- e.g.

- <https://www.wired.it/attualita/2018/01/11/ces-2018-gadget-strani/>

- <https://www.wired.it/gadget/accessori/2019/01/11/ces-2019-gadget-folli/>



# Embedded Systems Design: A Unified Hardware/Software Introduction

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Design challenge – optimizing design metrics

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# Design challenge: optimizing design metrics

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- Obvious design goal:
  - Construct an implementation with desired functionality
- Key design challenge:
  - Simultaneously optimize numerous design metrics
- Design metric
  - A measurable feature of a system's implementation
  - Optimizing design metrics is a key challenge

# Design challenge: optimizing design metrics

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- Common metrics
  - Unit cost: the monetary cost of manufacturing each copy of the system, excluding NRE cost
  - NRE cost (Non-Recurring Engineering cost): The one-time monetary cost of designing the system
  - Size: the physical space required by the system
    - Area (or % of available resources, e.g. memory)
  - Performance: the execution time or throughput of the system
    - Latency, Throughput, Speedup, ...
  - Power/Energy: the amount of power/energy consumed by the system
  - Flexibility: the ability to change the functionality of the system without incurring heavy NRE cost
  - ...

# Considerations

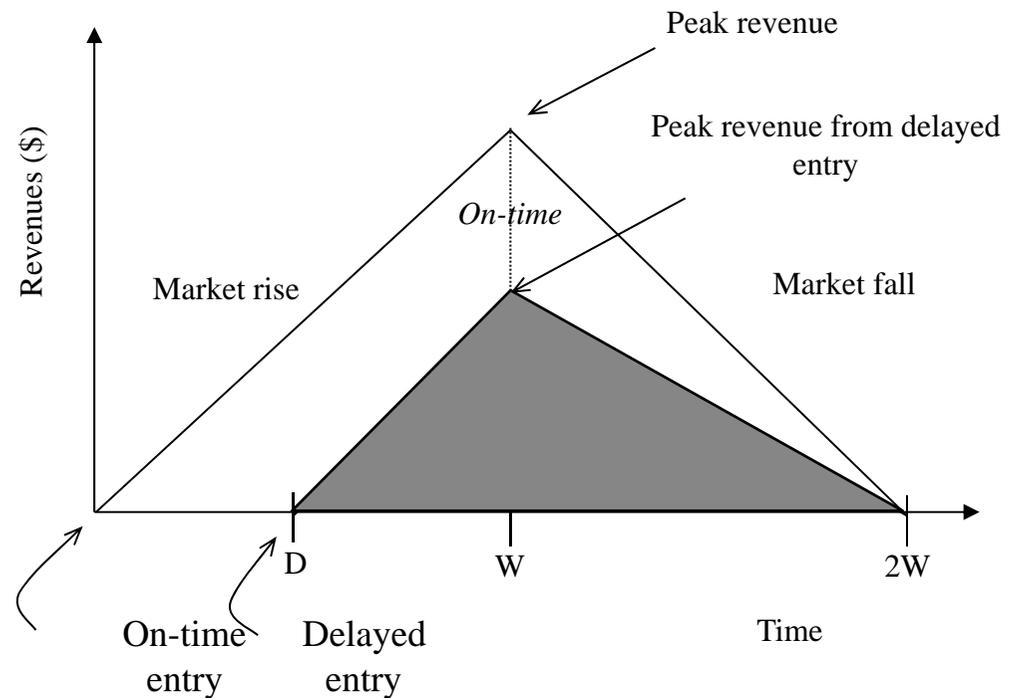
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- A designer could think that F/NF aspects driving the design choices are only related to technology issues but, in the real industrial life, the ability to exploit the market at the best moment is often the most important one!
  - It is needed to select technologies and design methodologies that lead to the max sales volume and max profit
    - Rarely this means to exploit the most advanced technologies...
    - Not always this means to find the most cheaper design!

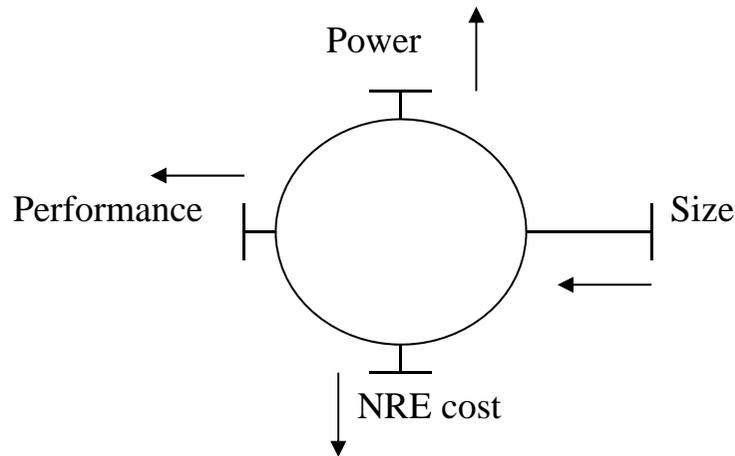
# Considerations

- Time-to-market
  - Sales volume loss

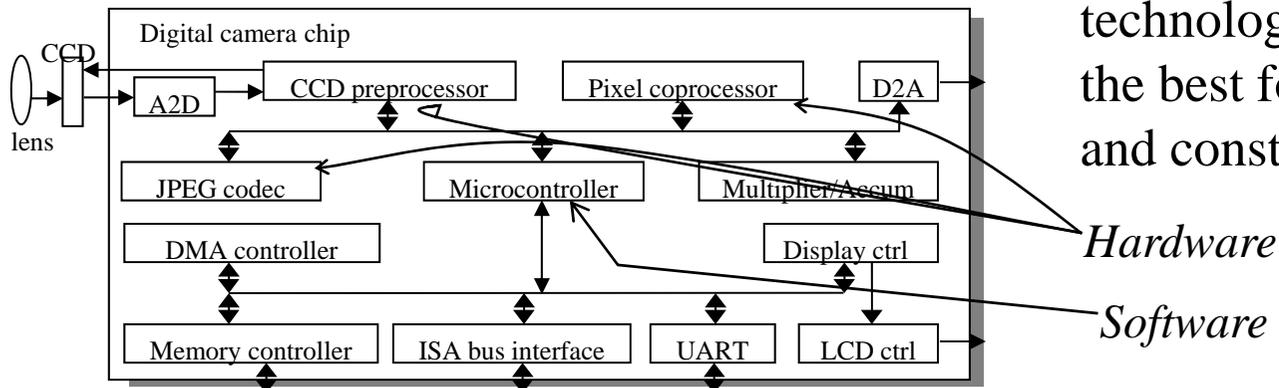
$$R_L = R_0 \frac{D(3W - D)}{2W^2}$$



# Design metric competition: improving one may worsen others



- Expertise with both **software** and **hardware** is needed to optimize design metrics
  - Not just a hardware or software expert, as is common
  - A designer must be comfortable with various technologies in order to choose the best for a given application and constraints



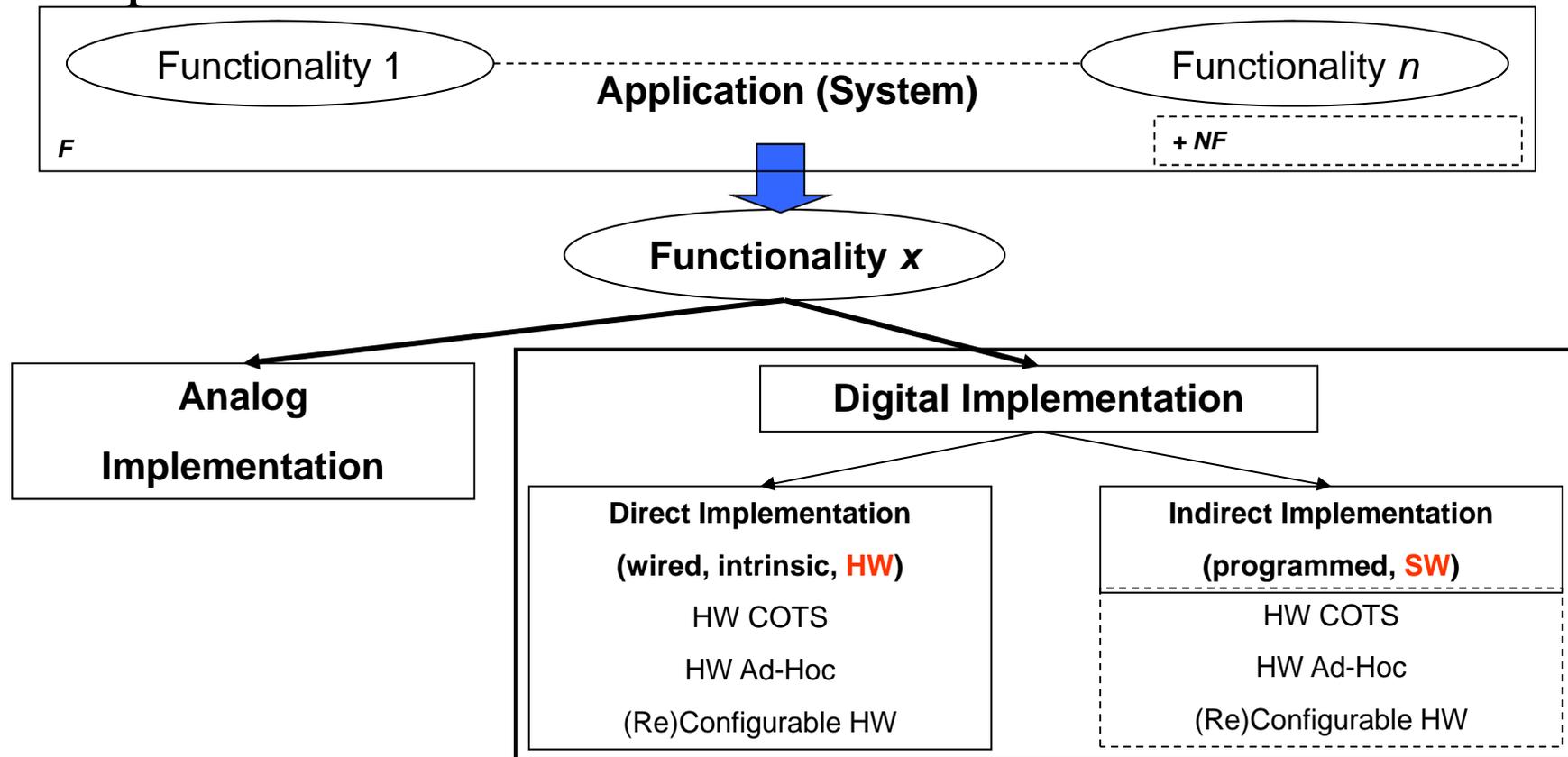
# Considerations

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- Given the same F requirements, the best technologies could be very different depending on NF ones
  - The whole HW/SW architecture could be very different too!
- For this, it is quite difficult to standardize embedded system architectures (as normally done for general-purpose systems)
  - Some exceptions could be found in specific domains
    - e.g. the AUTOSAR reference architecture in the automotive domain
- This exacerbate the need for cross-competence designers!

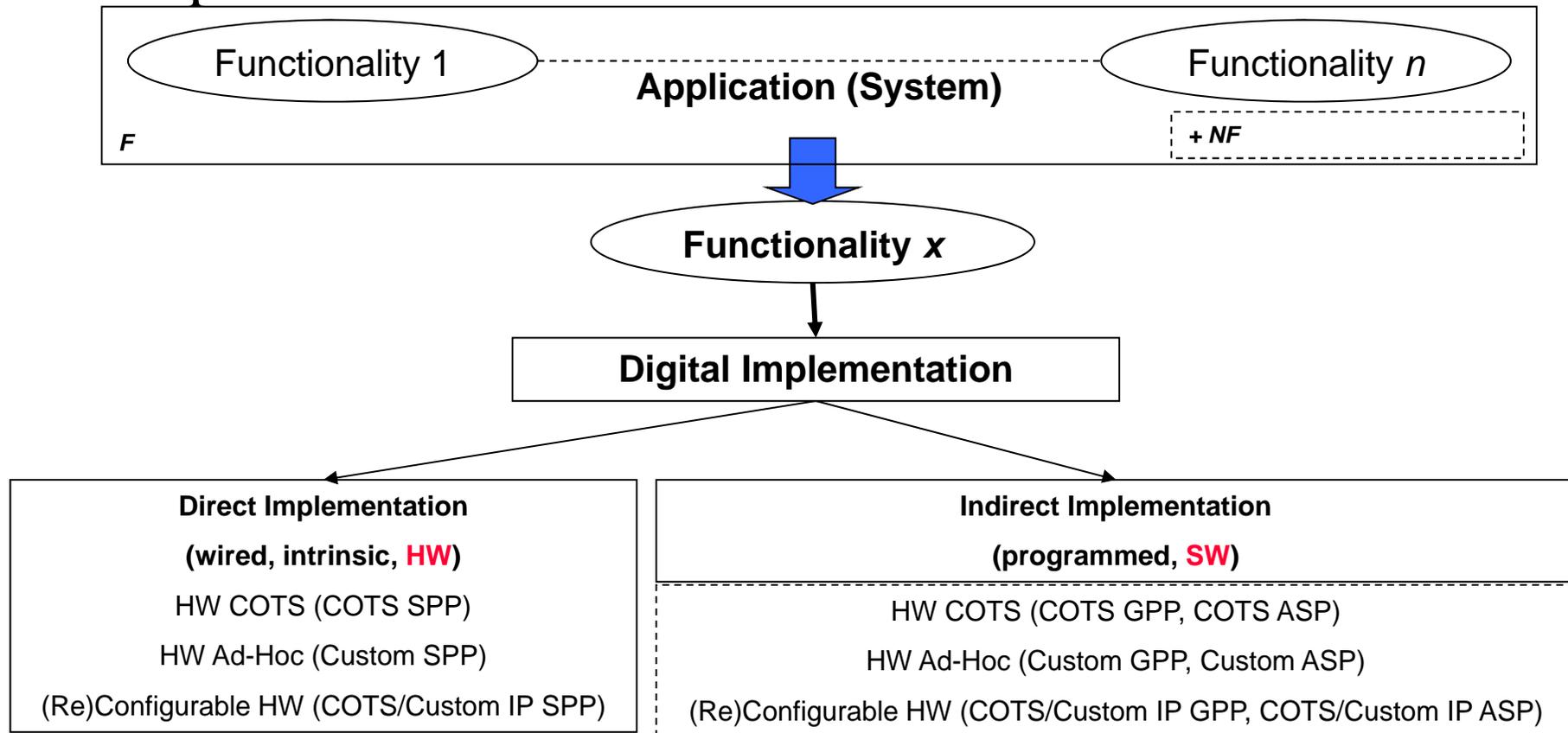
# Considerations

- Implementation alternatives



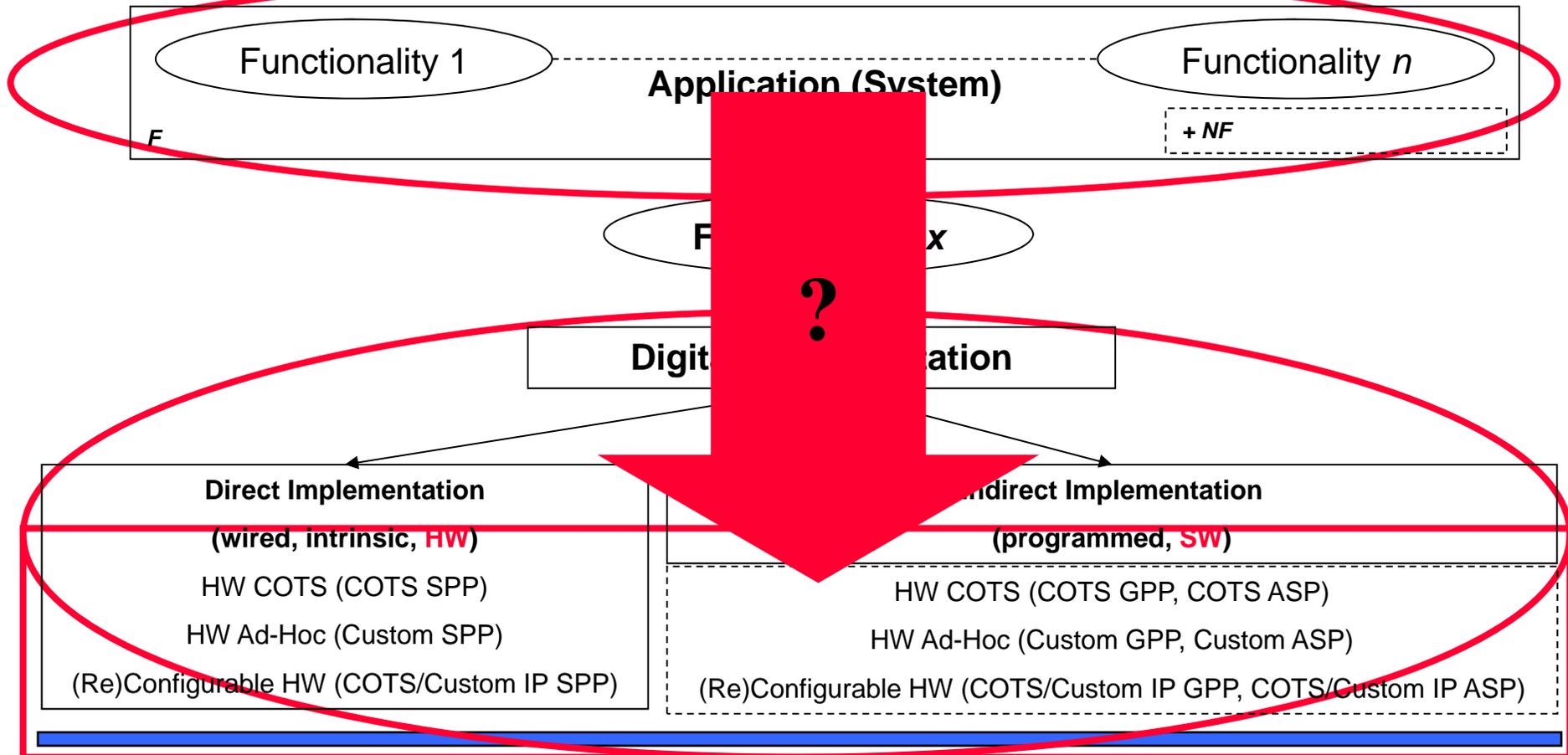
# Considerations

- Implementation alternatives



# Considerations

- HW/SW Architectures



# Embedded Systems Design: A Unified Hardware/Software Introduction

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Technologies

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# Three key embedded system technologies

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- Technology
  - A manner of accomplishing a task, especially using technical processes, methods, or knowledge
- Three key technologies for embedded systems
  - IC technology
  - Processor technology
  - Design technology

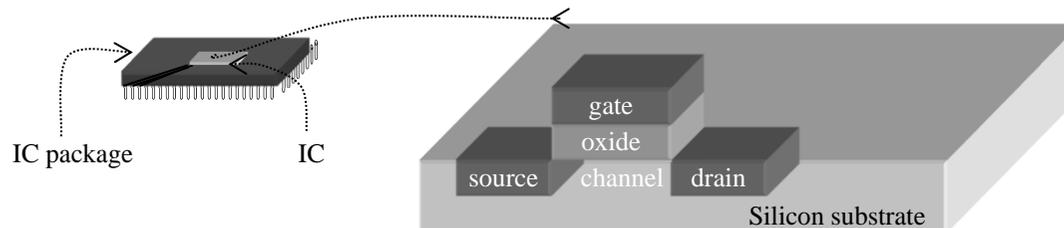
# Three key embedded system technologies

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- Technology
  - A manner of accomplishing a task, especially using technical processes, methods, or knowledge
- Three key technologies for embedded systems
  - **IC technology**
    - In which way can we realize processors?
  - Processor technology
  - Design technology

# IC technology

- The manner in which a digital implementation is mapped onto an IC
  - IC: Integrated circuit, or “chip”
  - IC technologies differ in their customization to a design
  - IC’s consist of numerous layers (perhaps 10 or more)
    - IC technologies differ with respect to who builds each layer and when



# IC technology

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- Three types of IC technologies
  - Full-custom/VLSI
  - Semi-custom ASIC (gate array and standard cell)
  - PLD (Programmable Logic Device)

# Full-custom/VLSI

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- All layers are optimized for an embedded system's particular digital implementation
  - Placing transistors
  - Sizing transistors
  - Routing wires
- Benefits
  - Excellent performance, small size, low power
- Drawbacks
  - High NRE cost, long time-to-market

# Semi-custom

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- Lower layers are fully or partially built
  - Designers are left with routing of wires and maybe placing some blocks
- Benefits
  - Good performance, good size, less NRE cost than a full-custom implementation
- Drawbacks
  - Still require weeks to months to develop

# PLD (Programmable Logic Device)

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- All layers already exist
  - Designers can purchase an IC
  - Connections on the IC are either created or destroyed to implement desired functionality
  - Field-Programmable Gate Array (FPGA) very popular
- Benefits
  - Low NRE costs, almost instant IC availability
- Drawbacks
  - Bigger, expensive (near 10/300 €per unit for a low/high FPGA chip), power hungry, slower

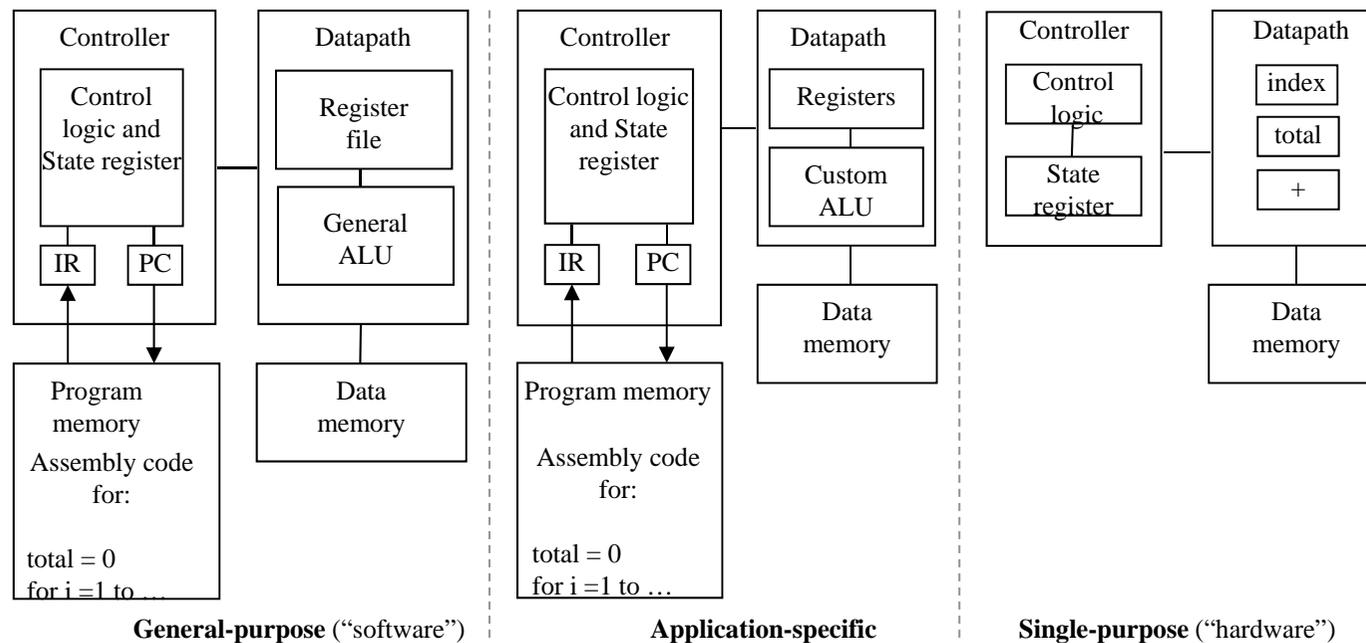
# Three key embedded system technologies

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- Technology
  - A manner of accomplishing a task, especially using technical processes, methods, or knowledge
- Three key technologies for embedded systems
  - IC technology
    - In which way can we realize processors?
  - **Processor technology**
    - In which way can we perform computations?
  - Design technology

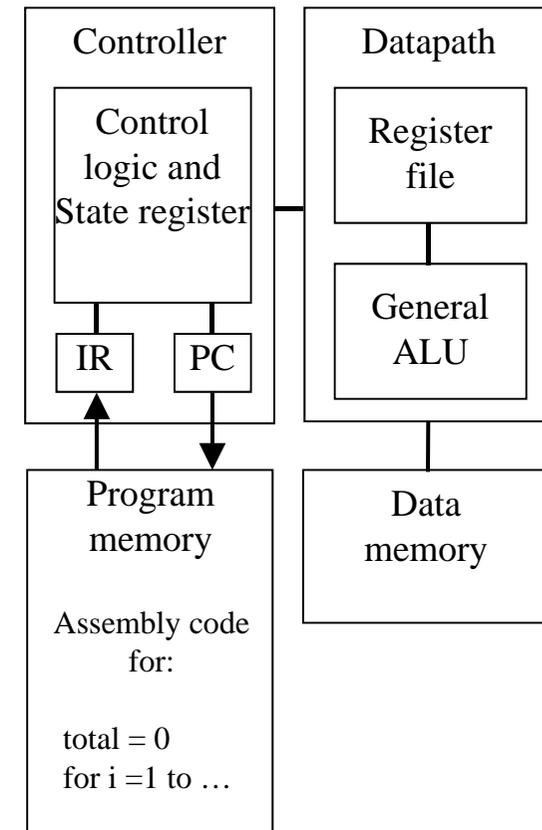
# Processor technology

- The architecture of the computation engine used to implement a system's desired functionality
- Processor does not have to be programmable
  - “Processor” *not* equal to general-purpose processor



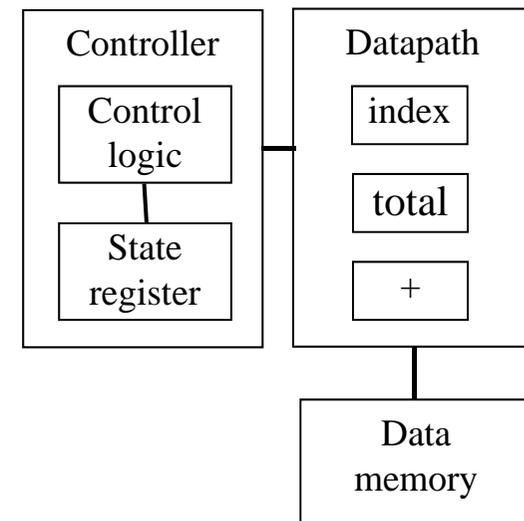
# General-purpose processors

- Programmable device used in a variety of applications
  - Also known as “microprocessor”
- Features
  - Program memory
  - General datapath with large register file and general ALU
- User benefits
  - Low time-to-market and NRE costs
  - High flexibility
- “Pentium” the most well-known, but there are hundreds of others



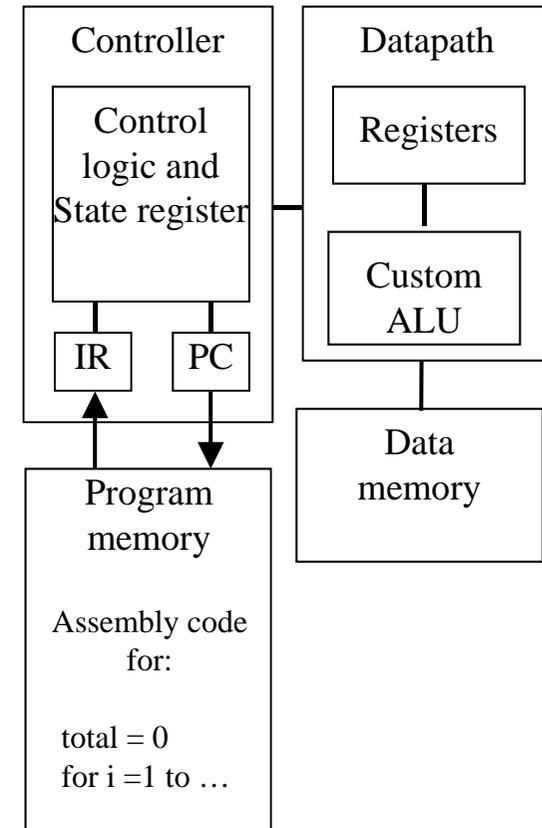
# Single-purpose processors

- Digital circuit designed to execute exactly one “program”
  - a.k.a. coprocessor, accelerator or peripheral
- Features
  - Contains only the components needed to execute a single program
  - No program memory
- Benefits
  - Fast
  - Low power
  - Small size



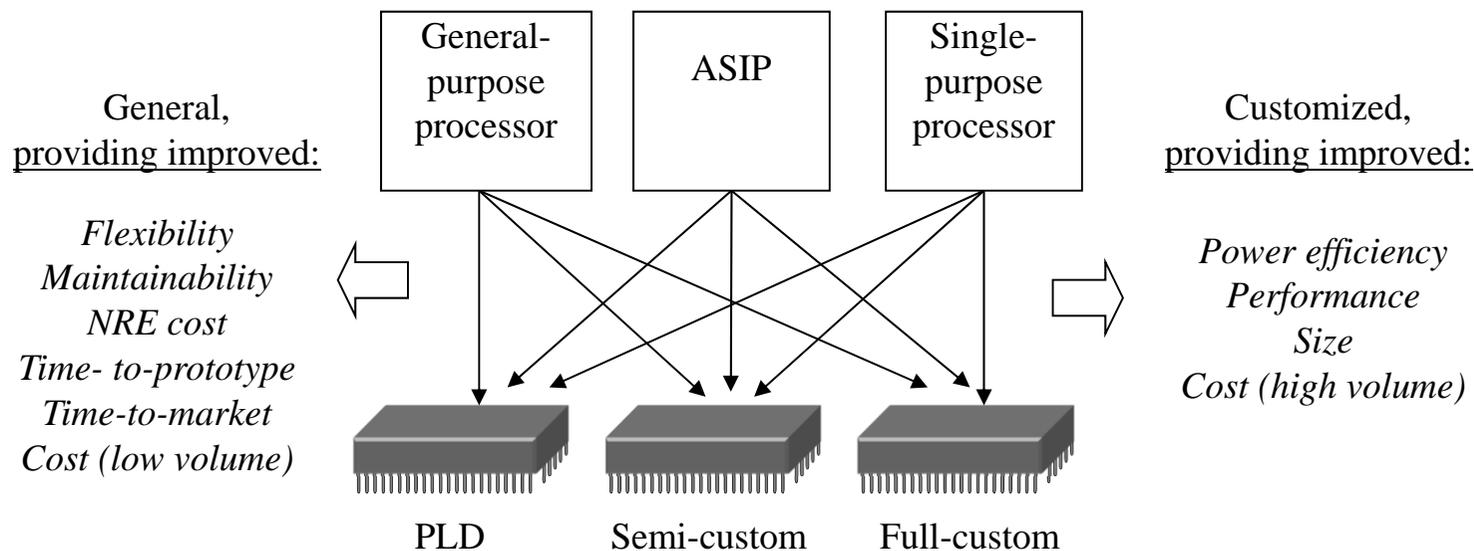
# Application-specific processors

- Programmable processor optimized for a particular class of applications having common characteristics
  - Compromise between general-purpose and single-purpose processors
- Features
  - Program memory
  - Optimized datapath
  - Special functional units
- Benefits
  - Some flexibility, good performance, size and power



# Independence of processor and IC technologies

- Basic tradeoff
  - General vs. custom
  - With respect to processor technology or IC technology
  - The two technologies are independent



What is it able to do?

NO ISA  
(SPP/HW)

GPP

AS(IP)

ISA (SW)

Processor

Component

Logical  
Component  
(Description)

Physical  
Component  
(Implementation)

How is it realized?

PLD  
(FPGA)

(AS)IC  
-Full Custom  
-Semi-Custom

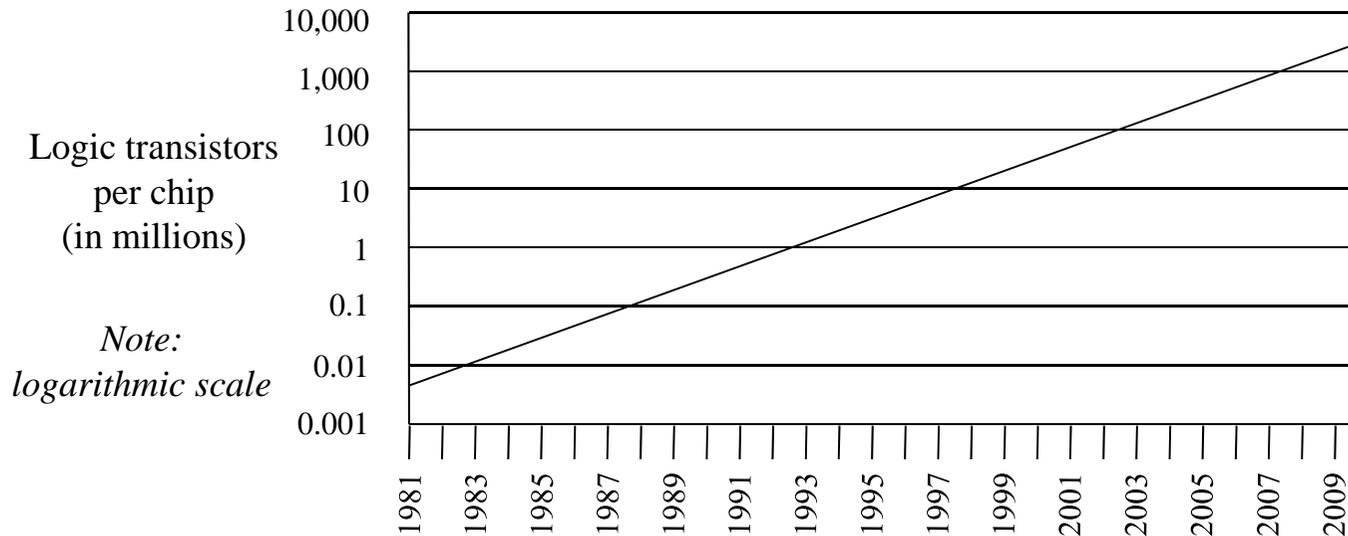
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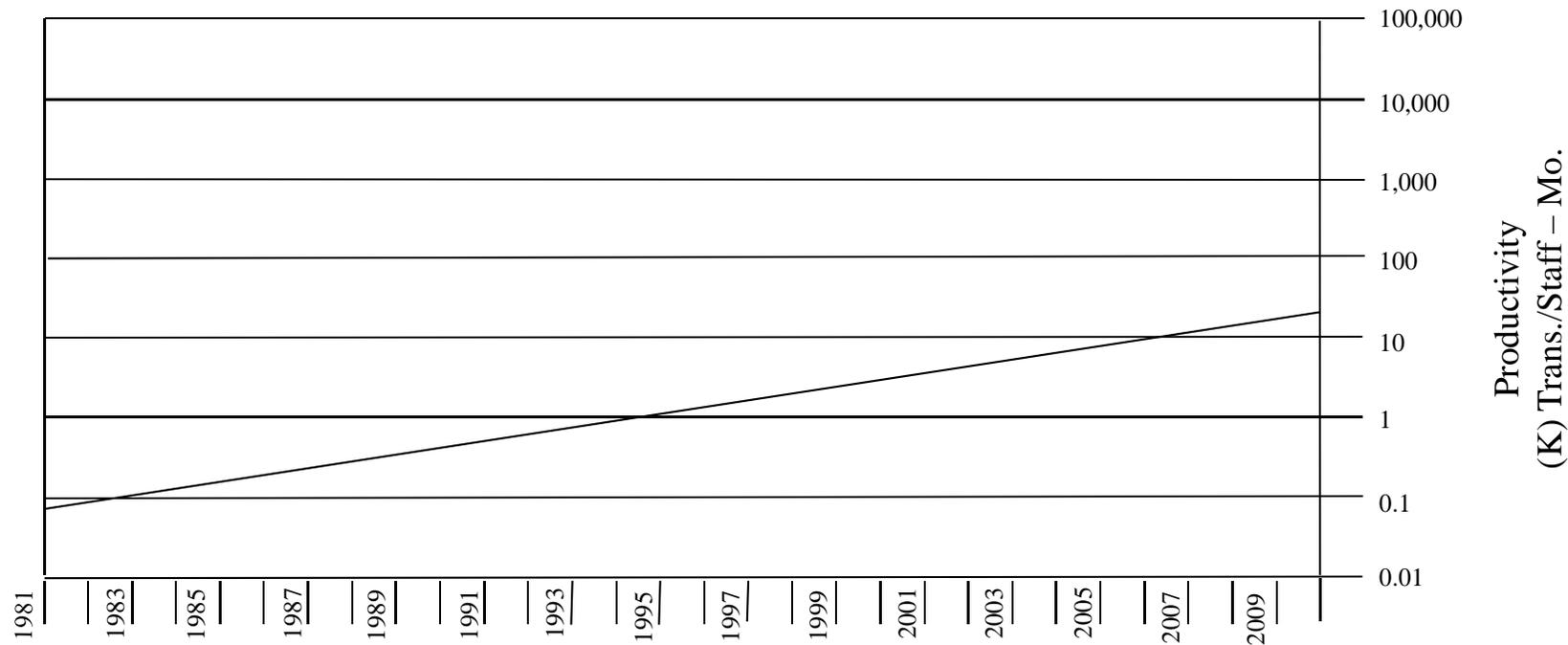
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  - Processor technology
    - In which way can we perform computations?
  - **Design technology**
    - In which way can we design and exploit processors and their combinations?

# Moore's law

- The most important trend in embedded systems
    - Predicted in 1965 by Intel co-founder Gordon Moore
- IC transistor capacity has doubled roughly every 18 months for the past several decades***



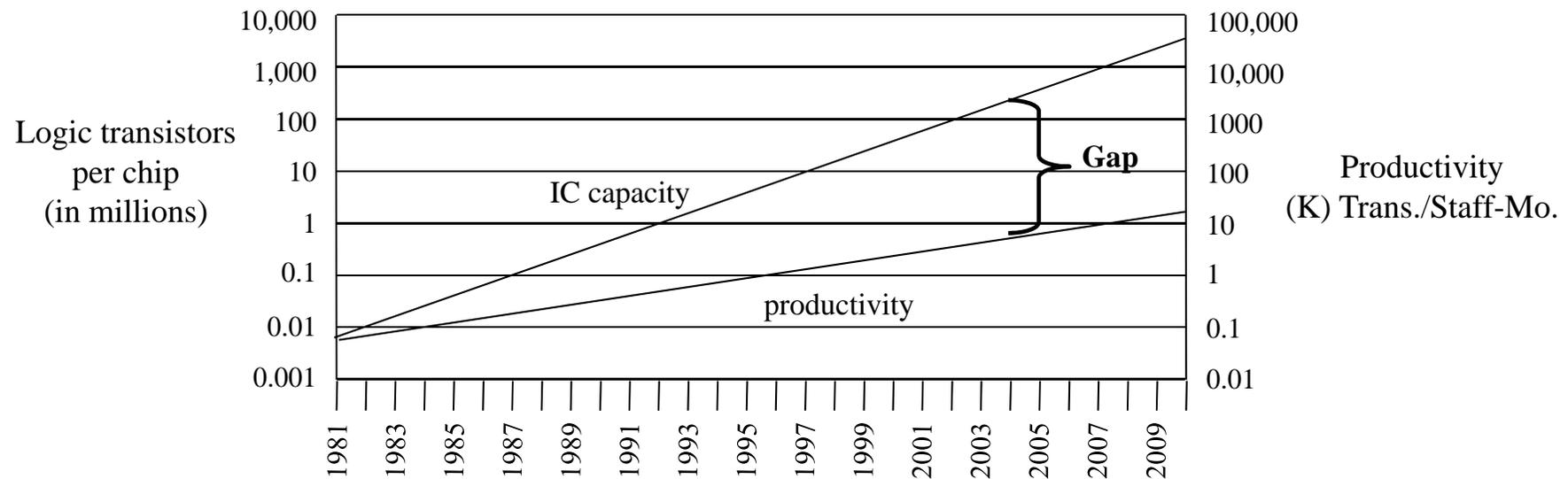
# Design productivity exponential increase



- Exponential increase over the past few decades

# Design productivity gap

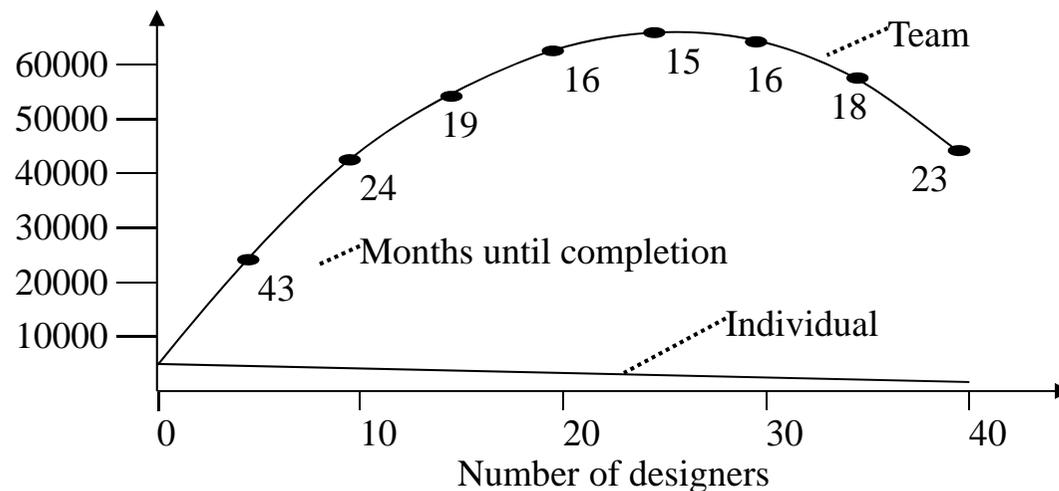
- While designer productivity has grown at an impressive rate over the past decades, the rate of improvement has not kept pace with chip capacity



# The mythical man-month

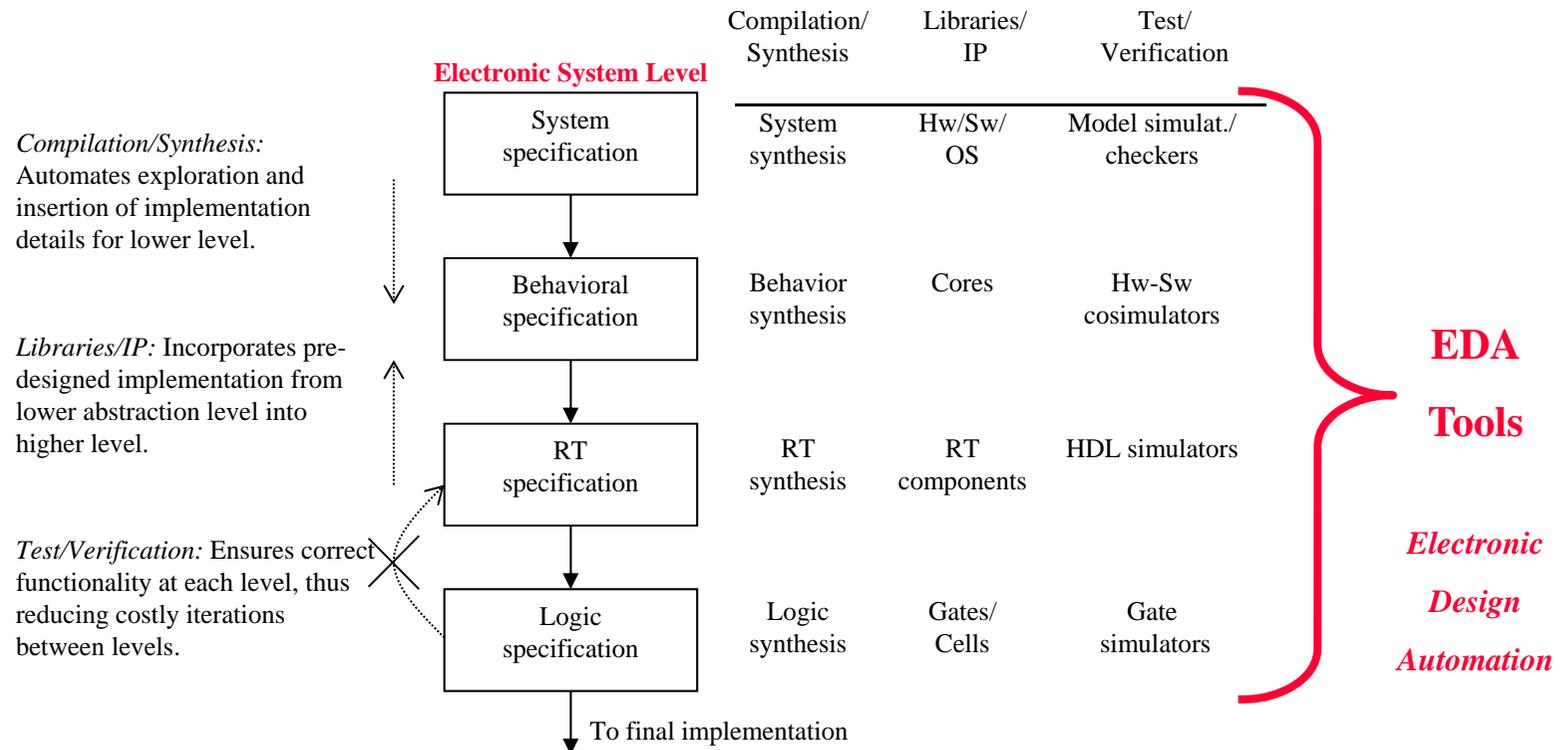
- The situation is even worse than the productivity gap indicates
- In theory, adding designers to team reduces project completion time
- In reality, productivity per designer decreases due to complexities of team management and communication
- In the software community, known as “the mythical man-month” (Brooks 1975)
- At some point, can actually lengthen project completion time! (“Too many cooks”)

- 1M transistors, 1 designer=5000 trans/month
- Each additional designer reduces for 100 trans/month
- So 2 designers produce 4900 trans/month each



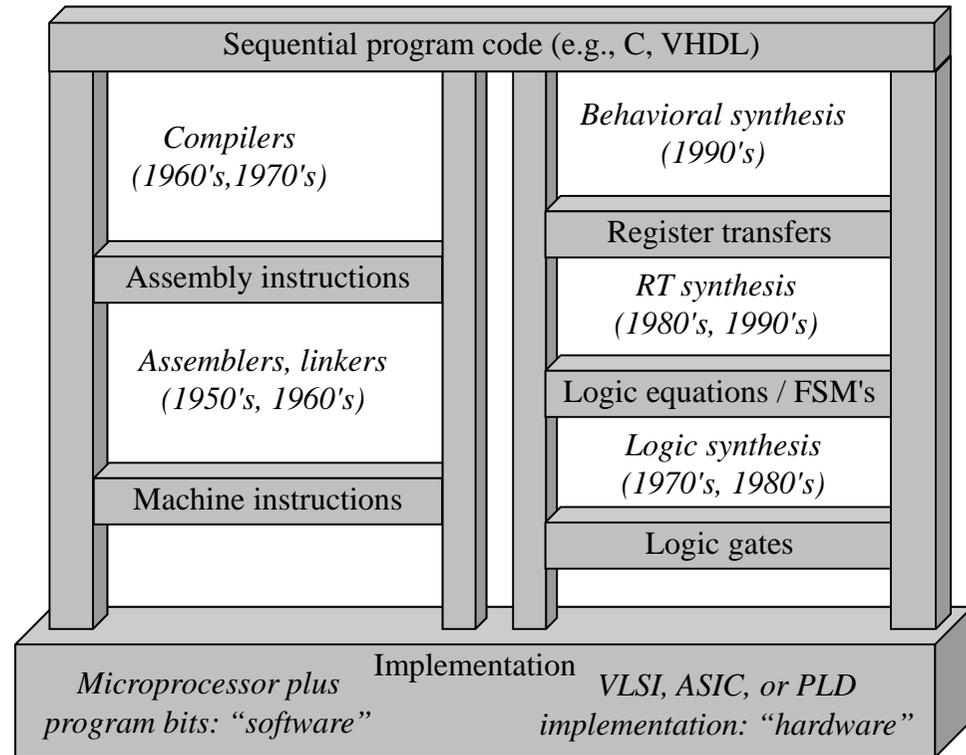
# Design Technology

- The manner in which we convert our concept of desired system functionality into an implementation



# The co-design ladder

- In the past:
  - Hardware and software design technologies were very different
  - Recent maturation of synthesis enables a unified view of hardware and software
- Hardware/software “codesign”



*The choice of hardware versus software for a particular function is simply a tradeoff among various design metrics, like performance, power, size, NRE cost, and especially flexibility; there is no fundamental difference between what hardware or software can implement.*

# Considerations

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- Embedded systems development is still far from being a discipline with mature and general design methodologies
  - SW Engineering vs ES Engineering
- It requires cross-competences and good knowledge of the applicative domain

# Considerations

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- An helpful improvement is currently provided by the so called *Platform Based Design* approach
  - The goal is to define and/or exploit domain-oriented HW/SW architectures (i.e. the platforms) that could be easily re-used for all the applications of a given domain
    - e.g. Automotive: *AUTOSAR*, *Aeronautics: ARINC*,...
      - A platform can be
        - Virtual: it is a set of models of the different parts of the system (may be executable, analyzable, synthesizable,...)
        - Real: it is a set of well known existing hw/sw components
    - Benefits: re-use of basic blocks already validated and commercially available to move the design effort at higher levels of abstraction
    - Drawbacks: less customizable/optimizable

# Summary

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- Embedded systems are everywhere
- Key challenge: optimization of design metrics
  - Design metrics compete with one another
- A unified view of hardware and software is necessary to improve productivity
- Three key technologies
  - IC: Full-custom, semi-custom, PLD
  - Processor: general-purpose, application-specific, single-purpose
  - Design: Compilation/synthesis, libraries/IP, test/verification